



Recent advances with the VSM algorithm - a building block for a stable and well-damped electronic grid

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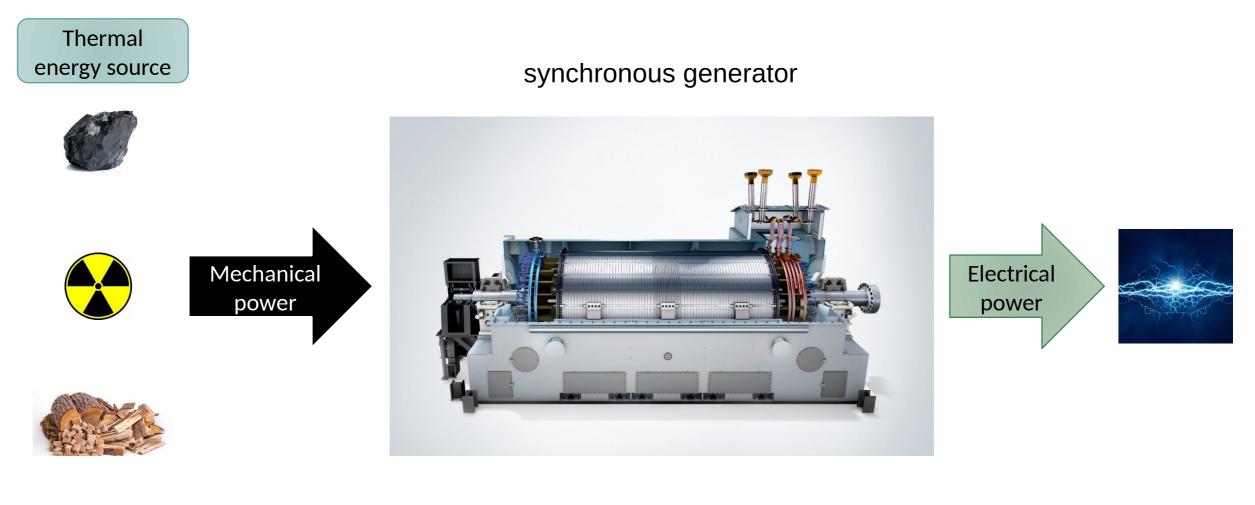
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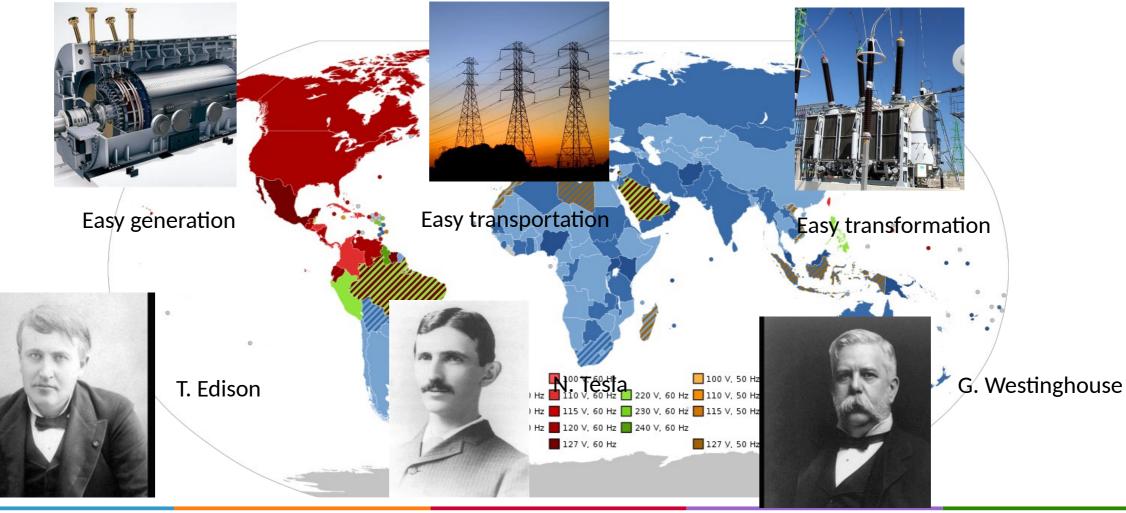






Most commonly grids are AC

Reasons for using AC:





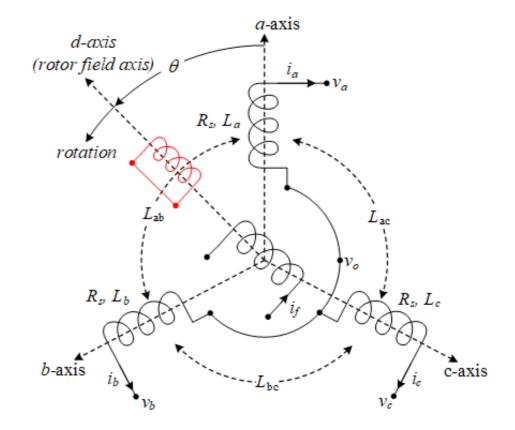


Synchrounous genartors (SG)

SG external appearance:

SG schematic representation:

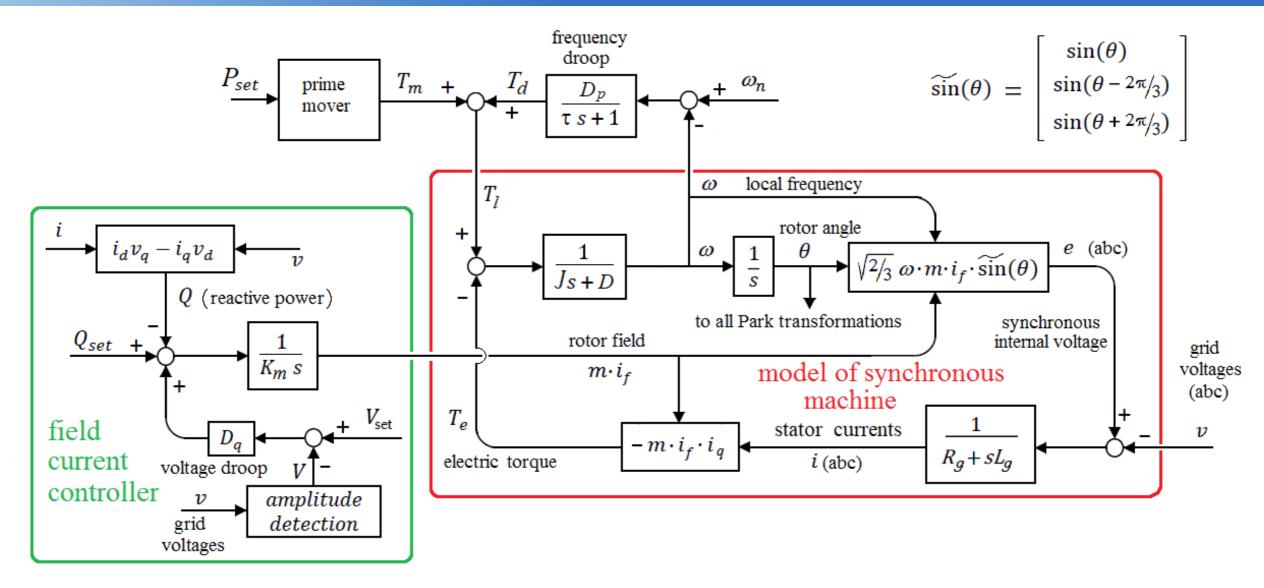








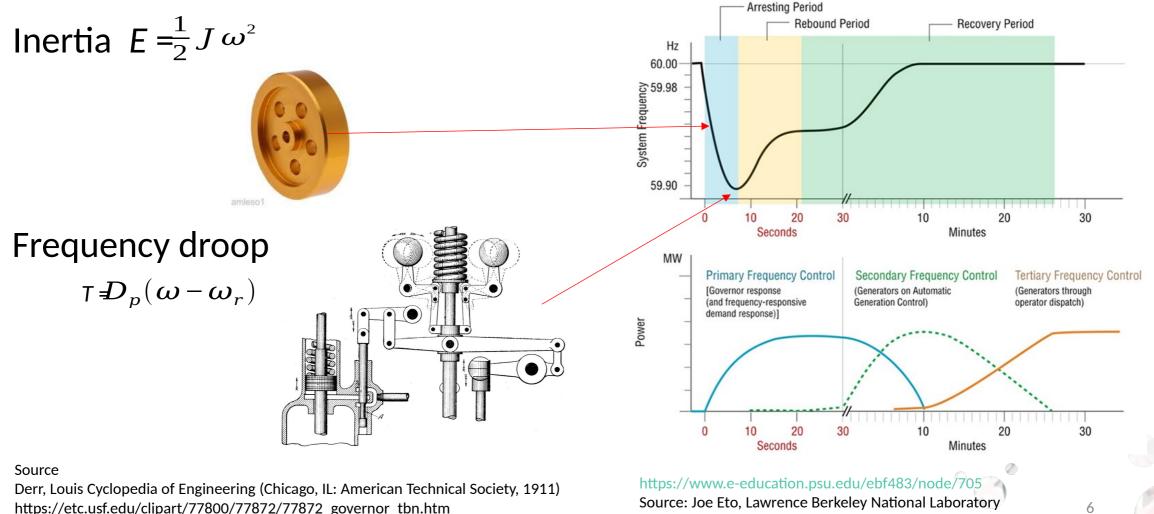
Simplified block diagram of a conventional SG with excitation control:







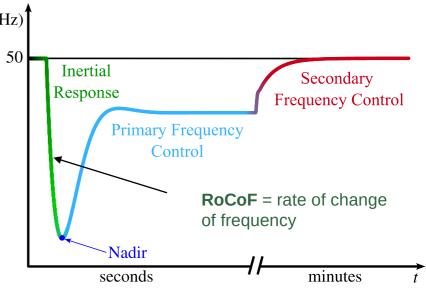
SGs connected together in a grid tend to synchronize! First explanation in 2012 (DB). Two main stabilizing mechanisms:



Risks of conventional (power source) control for grid power converters

The frequency response in case of sudden overload (or generator loss) for a grid dominated by synchronous generators: f(Hz)

- The frequency drops to a minimum value (Nadir)
- The Nadir and RoCoF (Rate of Change of Frequency) depends on the energy stored in the total rotating mass (rotors of generators and turbines)
- The droop control takes action (primary frequency control)
- The frequency goes back to rated value thanks to the (slow) action of the secondary frequency control



In a grid with large penetration of static power converters, the total system inertia is drastically lowered and therefore the RoCoF increases while Nadir may drop too much, triggering protection systems for load shedding or generator disconnection, with the risk of losing stability.





SG vs power electronic (PE) converters:

SG characteristics:

- Inertial behavior (mechanical rotor)
- Large short circuit current contribution
- Harmonic filtering



Conventional PE converters

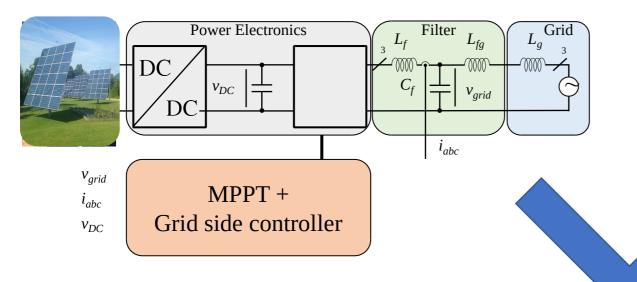
- No inertia (no rotating parts)
- Limited short circuit current (rated)
- Active filters for power quality improvement







Conventional solar inverter:



VSM using some energy storage + same power electronics hardware:

VSM

Control

Control algorithm:

- Virtual inertia (mechanical emulation)
- Reactive support during faults
- Harmonic compensation

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• Frequency & voltage droops





 v_{qrid}

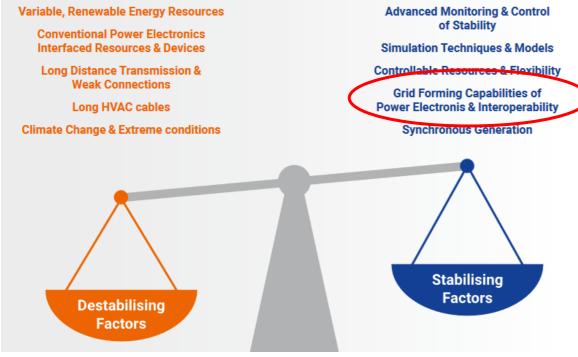
*i*_{abc}

VSM Fields of Application

Renewable energy integration

- Solar and wind farms
- Storage systems

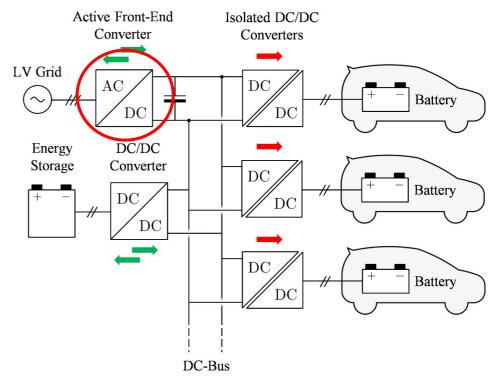
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ENTSO-E, "Stability Management in Power Electronics Dominated Systems: A Prerequisite to the Success of the Energy Transition", June 2022

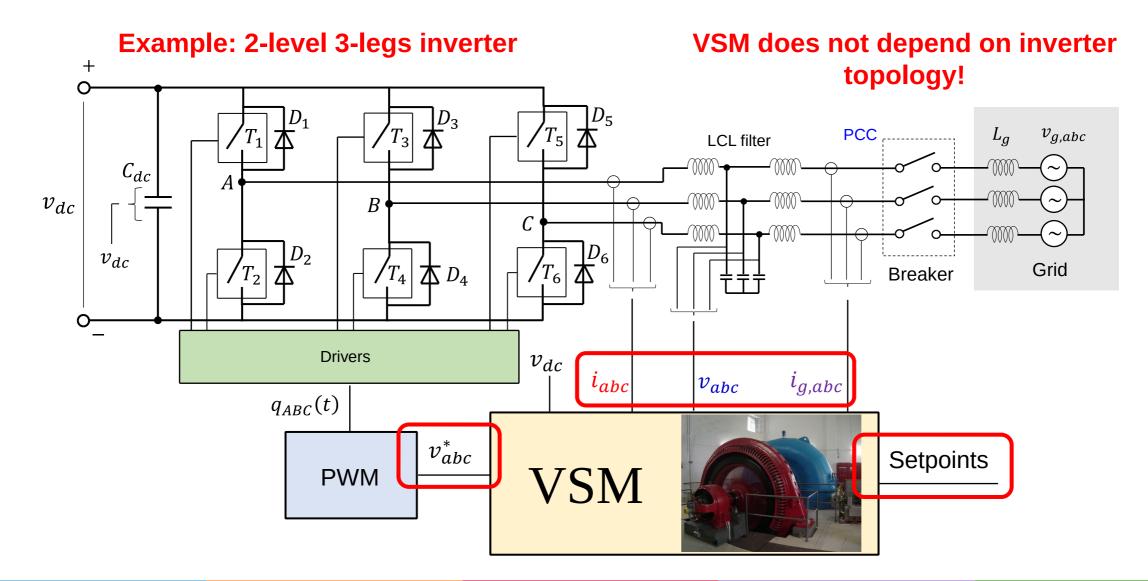
Large aggregated loads

 Fast charging stations with V2G capability: provision of reactive power support, droops and inertia





Inverter architecture







Hardware and low-level control - two alternatives:

On ms scale, voltage source behavior:

- Simple control (open loop voltage)
 More suited for weak grids
- Difficult to limit current
- Stator inductance = Grid-side filter
- Very sensitive to measurement errors

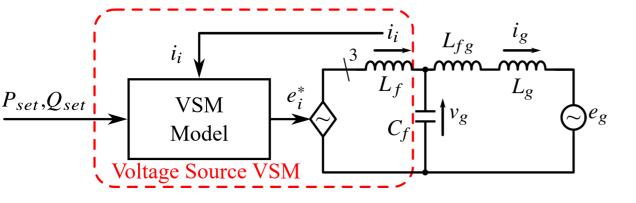


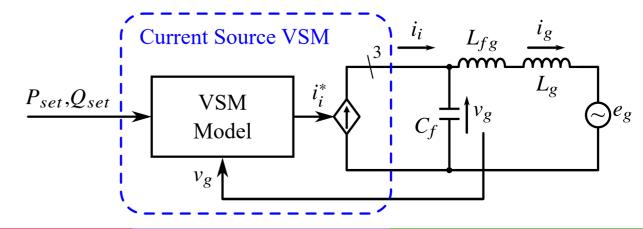


Current limitation (closed loop)



- Fully tunable virtual stator
- More complex (current control)
- Issues with ultra-weak grids





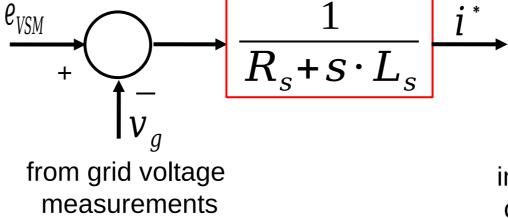


How to generate the reference output current i^* in a VSM

The version with current control is much better for immunity to noise and imbalance. To use it, we need to create a virtual impedance in the VSM, which in itself is a plus:

- Can be implemented in abc, $\alpha\beta$ or dq frames
- Avoids numerical differentiation and low pass filter
- We can make the stator impedance much larger than the inverter filter impedance
- Current limitation is possible, without introducing distortion (clipping)

synchronous internal voltage from VSM algorithm, depends on excitation control



current limitation and then current loop references

We may use more complex impedance comprising virtual capacitor, tunable elements.

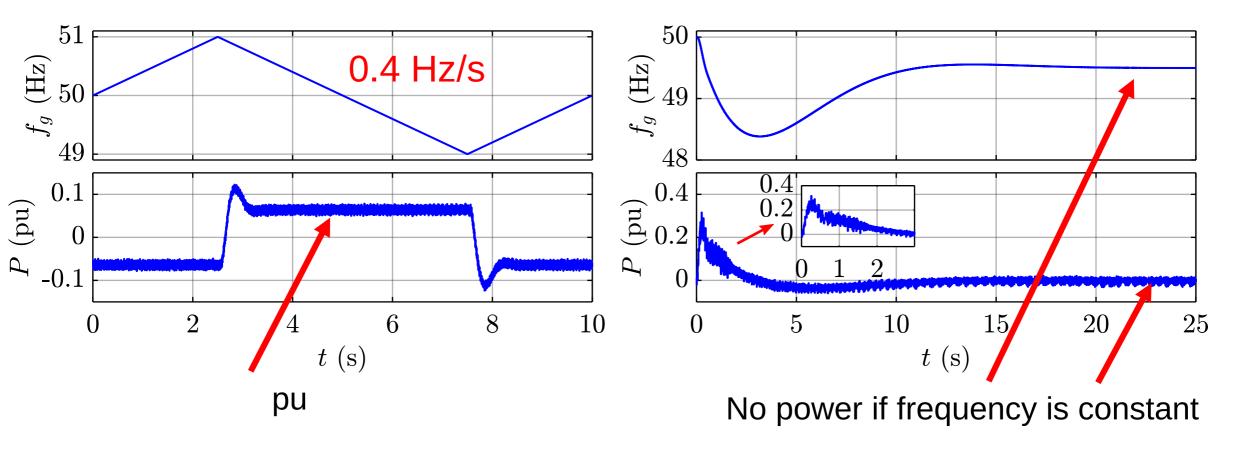




Frequency Support – test of inertial response

Triangular frequency Hz (s)

Emulating a real frequency drop (s)



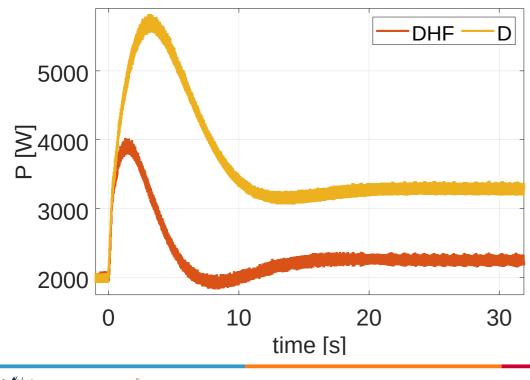




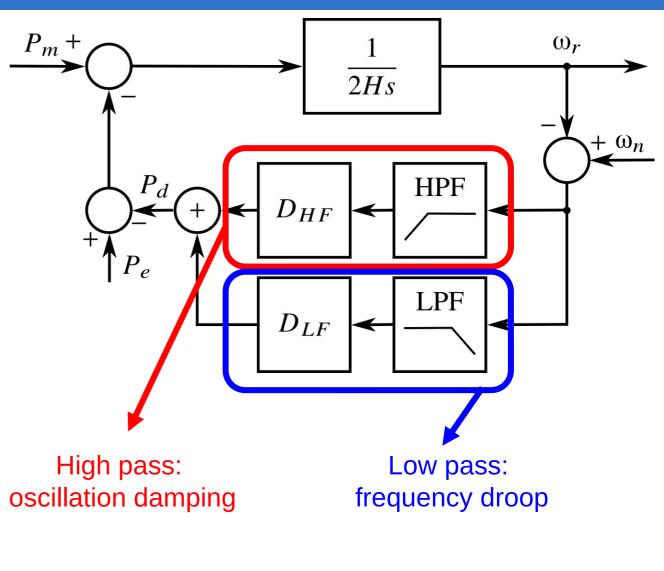
Splitting the frequency droop

Alternative damping solution:

• High-pass + low-pass filters



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Splitting the frequency droop into high and low frequency channels

The high frequency (HF) channel is for damping,

the low frequency (less gain) for primary support.

Alternative damping solution:

- Lead-lag filter damping
- Avoids coupling with frequency droop
- Lower sensitivity to high frequency compared to PLL- or PI-damping

